

Please amend claims 3-7 and 9 to read as follows:

1 3. (Amended) The controllable current source according to claim 1, wherein the current
2 adjustment device (26, 28) has at least one FET, whose channel forms a section of the current path (18,
3 20).

1 4. (Amended) The controllable current source according to claim 1, wherein the two
2 adjustment potentials (V_{aa} , V_{bb}) are different from one another.

1 5. (Amended) The controllable current source according to claim 1, wherein the adjustment
2 potential (V_{aa}) for the first current path (18) lies closer to the first supply potential (V_1) than the
3 adjustment potential (V_{bb}) for the second current path (20).

1 6. (Amended) The controllable current source according to claim 1, wherein the adjustment
2 potential (V_{aa} , V_{bb}) for one of the two current paths (18, 20) is approximately equal to that potential
3 which is present at the intermediate section (30, 32) of this current path (18, 20) when the current control
4 device (22, 24) of this current path (18, 20) is activated.

1 7. (Amended) The controllable current source according to claim 1, wherein the potential
2 adjustment device (40₁, 40₂) supplies the adjustment potential (V_{aa} , V_{bb}) between the channels of two
3 FETs forming a voltage divider.

1 9. (Amended) A controllable voltage source, comprising a controllable current source (10)
2 according to claim 1 and a downstream integrator (60).